











LMK61E2-100M, LMK61E2-125M, LMK61E2-156M, LMK61E2-312M LMK61A2-100M, LMK61A2-125M, LMK61A2-156M, LMK61A2-312M, LMK61I2-100M

SNAS676 - OCTOBER 2015

LMK61XX High-Performance Ultra-Low Jitter Oscillator

1 Features

- Ultra-low Noise, High Performance
 - Jitter: 90 fs RMS typical Fout > 100 MHz
 - PSRR: -70 dBc, robust supply noise immunity
- Supported Output Format
 - LVPECL up to 1 GHz
 - LVDS up to 900 MHz
 - HCSL up to 400 MHz
- Total frequency tolerance of ± 50 ppm
- 3.3 V operating voltage
- Industrial temperature range (-40°C to +85°C)
- 7 mm x 5 mm 6-pin package that is pincompatible with industry standard 7050 XO package

2 Applications

- High-performance replacement for crystal-, SAW-, or silicon-based Oscillators
- Switches, Routers, Network Line Cards, Base Band Units (BBU), Servers, Storage/SAN
- · Test and Measurement
- Medical Imaging
- FPGA, Processor Attach

3 Description

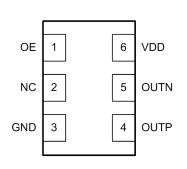
The LMK61XX is an ultra-low jitter oscillator that generates a commonly used reference clock. The device is pre-programmed in factory to support any reference clock frequency; supported output formats are LVPECL up to 1 GHz, LVDS up to 900 MHz, and HCSL up to 400 MHz. Internal power conditioning provide excellent power supply ripple rejection (PSRR), reducing the cost and complexity of the power delivery network. The device operates from a single $3.3~V \pm 5\%$ supply.

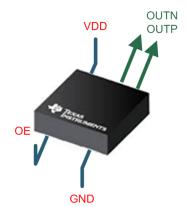
Device Information⁽¹⁾

PART NUMBER	OUTPUT FREQ (MHz) AND FORMAT	PACKAGE	BODY SIZE (NOM)		
LMK61A2- 100M00SIA	100 LVDS 125 LVDS				
LMK61A2- 125M00SIA					
LMK61A2- 156M25SIA	156.25 LVDS				
LMK61A2- 312M50SIA	312.5 LVDS		7.0 mm x 5.0 mm		
LMK61E2- 100M00SIA	100 LVPECL	6-pin QFM			
LMK61E2- 125M00SIA	125 LVPECL				
LMK61E2- 156M25SIA	156.25 LVPECL				
LMK61E2- 312M50SIA	312.5 LVPECL				
LMK61I2- 100M00SIA	100 HCSL				

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Pinout







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4 Device and Documentation Support

4.1 Related Links

The table below lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

Table 1. Related Links

PARTS	PRODUCT FOLDER	SAMPLE & BUY	TECHNICAL DOCUMENTS	TOOLS & SOFTWARE	SUPPORT & COMMUNITY	
LMK61E2-100M	Click here	Click here	Click here	Click here	Click here	
LMK61E2-125M	Click here	Click here	Click here	Click here	Click here	
LMK61E2-156M	Click here	Click here	Click here	Click here	Click here	
LMK61E2-312M	Click here	Click here	Click here	Click here	Click here	
LMK61A2-100M	Click here	Click here	Click here	Click here	Click here	
LMK61A2-125M	Click here	Click here	Click here	Click here	Click here	
LMK61A2-156M	Click here	Click here	Click here	Click here	Click here	
LMK61A2-312M	Click here	Click here	Click here	Click here	Click here	
LMK61I2-100M	Click here	Click here	Click here	Click here	Click here	

4.2 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

TI E2E™ Online Community T's Engineer-to-Engineer (E2E) Community. Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

Design Support *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

4.3 Trademarks

E2E is a trademark of Texas Instruments.

4.4 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

4.5 Glossary

SLYZ022 — TI Glossary.

This glossary lists and explains terms, acronyms, and definitions.

5 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.





12-Oct-2015

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LMK61A2-100M00SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		
LMK61A2-125M00SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		
LMK61A2-156M25SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		
LMK61A2-312M50SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		
LMK61E2-100M00SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		
LMK61E2-125M00SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		
LMK61E2-156M25SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		
LMK61E2-312M50SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		
LMK61I2-100M00SIAT	PREVIEW	QFM	SIA	6	250	TBD	Call TI	Call TI	-40 to 85		

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.



PACKAGE OPTION ADDENDUM

12-Oct-2015

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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Products	Applications

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